



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2827

Gf/41

Applicant:

Shore K. Chakravorty

Title:

ELECTRONIC ASSEMBLY COMPRISING INTERPOSER WITH EMBEDDED CAPACITORS AND METHODS OF MANUFACTURE

Docket No.: 884.267US1

Filed: July 31, 2000

Examiner: Tuan Dinh

Serial No.: 09/628705

Due Date: N/A

Group Art Unit: 2827

Mail Stop Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard.
- ☒ A Supplemental Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), and copies of 3 cited documents.

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
Customer Number 21186

By: Ann M. McCrackin
Atty: Ann M. McCrackin
Reg. No. 42,858

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 3 day of June, 2004.

KACIA LEE
Name

Kacia Lee
Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
(GENERAL)

S/N 09/628705



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Kishore K. Chakravorty	Examiner:	Tuan Dinh
Serial No.:	09/628705	Group Art Unit:	2827
Filed:	July 31, 2000	Docket:	884.267US1
Title:	ELECTRONIC ASSEMBLY COMPRISING INTERPOSER WITH EMBEDDED CAPACITORS AND METHODS OF MANUFACTURE		
Assignee:	Intel Corporation	Customer No:	21186

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Supplemental Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Supplemental Information Disclosure Statement considered.

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Serial No : 09/628705

Filing Date: July 31, 2000

Title: ELECTRONIC ASSEMBLY COMPRISING INTERPOSER WITH EMBEDDED CAPACITORS AND METHODS OF MANUFACTURE

Assignee: Intel Corporation

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The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

KISHORE K. CHAKRAVORTY

By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
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Date June 2, 2004

By Ann M. McCrackin
Ann M. McCrackin
Reg. No. 42,858

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KACIA LEE
Name

Kacia Lee
Signature

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

Application Number	09/628705
Filing Date	July 31, 2000
First Named Inventor	Chakravorty, Kishore
Group Art Unit	2827
Examiner Name	Dinh, Tuan

Sheet 1 of 1

Attorney Docket No: 884.267US1

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
	US-5,736,448	04/07/1998	Saia, R. J., et al.	438	393	12/04/1995
	US-6,532,143	03/11/2003	Figuerola, D G., et al.	361	301.4	12/29/2000

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
	JP-10-163447	06/19/1998	Katou, Yoshitake, et al.	H01L	27/108	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
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EXAMINER**DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional) ² Applicant is to place a check mark here if English language Translation is attached